

C25

Patent

Attorney Docket No.: 2207/11273



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS : Thomas D. FLETCHER

SERIAL NO. : 09/893,871

FILED : June 29, 2001

PATENT NO. : 6,928,572 B2

ISSUED : August 9, 2005

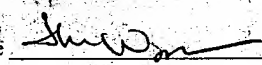
FOR : MULTISTAGE CLOCK DELAY CIRCUIT AND METHOD

GROUP ART UNIT : 2116

EXAMINER : Thuan Du

**Certificate
JAN 06 2006
of Correction**

M/S: CERTIFICATE OF
CORRECTION BRANCH
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate of Mailing	
I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: M/S: CERTIFICATE OF CORRECTION BRANCH, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on	
Date: December 27, 2005	Signature:  Thea Wagner

REQUEST FOR CERTIFICATE OF CORRECTION

Dear Sir:

We have compared the above patent with the application as filed and have found errors in the printing of the patent. We respectfully request that the enclosed Certificate of Correction on Form PTO-1050 be issued correcting the mistakes set forth therein under authority of 35 U.S.C. §254. The exact column and line number where the errors occurred in the patent are listed on the enclosed certificate.

JAN 09 2006

The errors that appear in the patent are typographical errors made by the Patent Office and therefore, no fee is required.

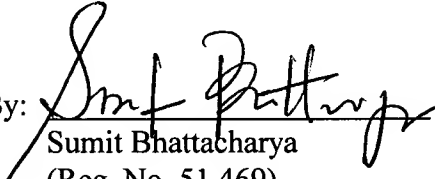
The Office is hereby authorized to charge any additional fees, or credit any overpayments, to Deposit Account No. **11-0600**.

Respectfully submitted,

KENYON & KENYON

Dated: December 27, 2005

By:


Sumit Bhattacharya
(Reg. No. 51,469)

KENYON & KENYON
333 West San Carlos St., Suite 600
San Jose, CA 95110

Telephone: (408) 975-7500
Facsimile: (408) 975-7501

JAN 09 2006

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO : 6,928,572 B2
DATED : August 9, 2005
INVENTOR(S) : Thomas D. FLETCHER et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 9, line 52, "to signal at the a clock"
should be --to the signal at a clock--;

Column 10, line 57, "delay block provide"
should be --delay block to provide--.

MAILING ADDRESS OF SENDER: Attorney Ref.: 2207/11273
KENYON & KENYON
333 W. San Carlos, Ste 600
San Jose, CA 95110
Telephone: (408) 975-7500

Date: Dec. 27, 2005

PATENT NO. 6,928,572 B2

No. of additional copies



This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

JAN 09 2006